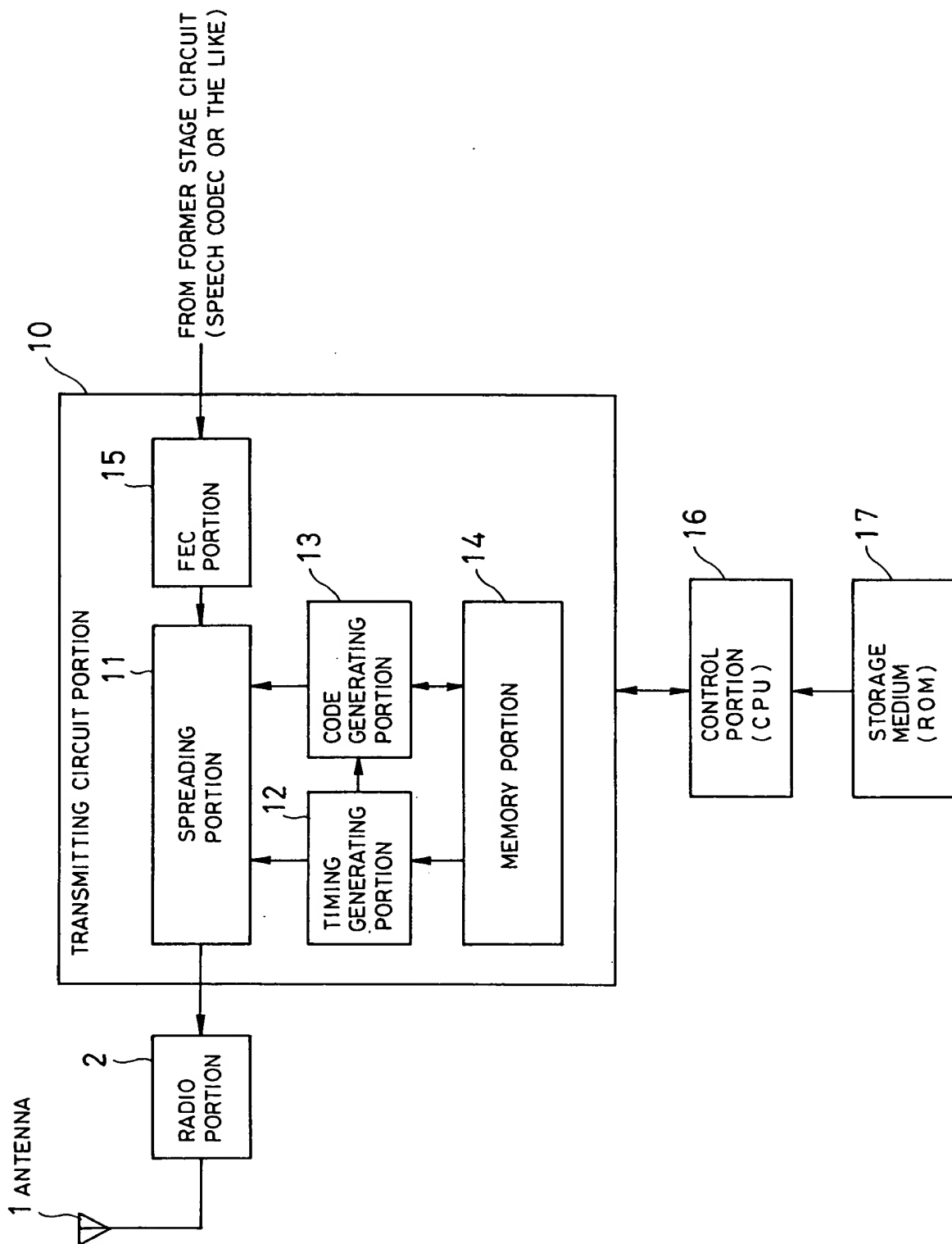


FIG.1



The diagram illustrates a radio receiving circuit portion (20) and its external connections. At the top, a '1 ANTENNA' is connected to a 'RADIO PORTION' (2). The output of the radio portion enters the 'FINGER RECEIVING CIRCUIT PORTION' (20). Inside this portion, the signal first passes through a 'DESPREADING PORTION' (21). This portion is connected to a 'TIMING GENERATING PORTION' (22) and a 'CODE GENERATING PORTION' (23). The timing generating portion (22) also receives input from a 'MEMORY PORTION' (24). The code generating portion (23) also receives input from the memory portion (24). The output of the despreading portion (21) is sent to a 'CHANNEL ESTIMATING PORTION' (25). The output of the channel estimating portion (25) is labeled 'TO LATER STAGE CIRCUIT (RAKE COMPOSITION)'. The entire finger receiving circuit portion (20) is connected to a 'CONTROL PORTION (CPU)' (26), which in turn is connected to a 'STORAGE MEDIUM (ROM)' (27).

FIG. 3

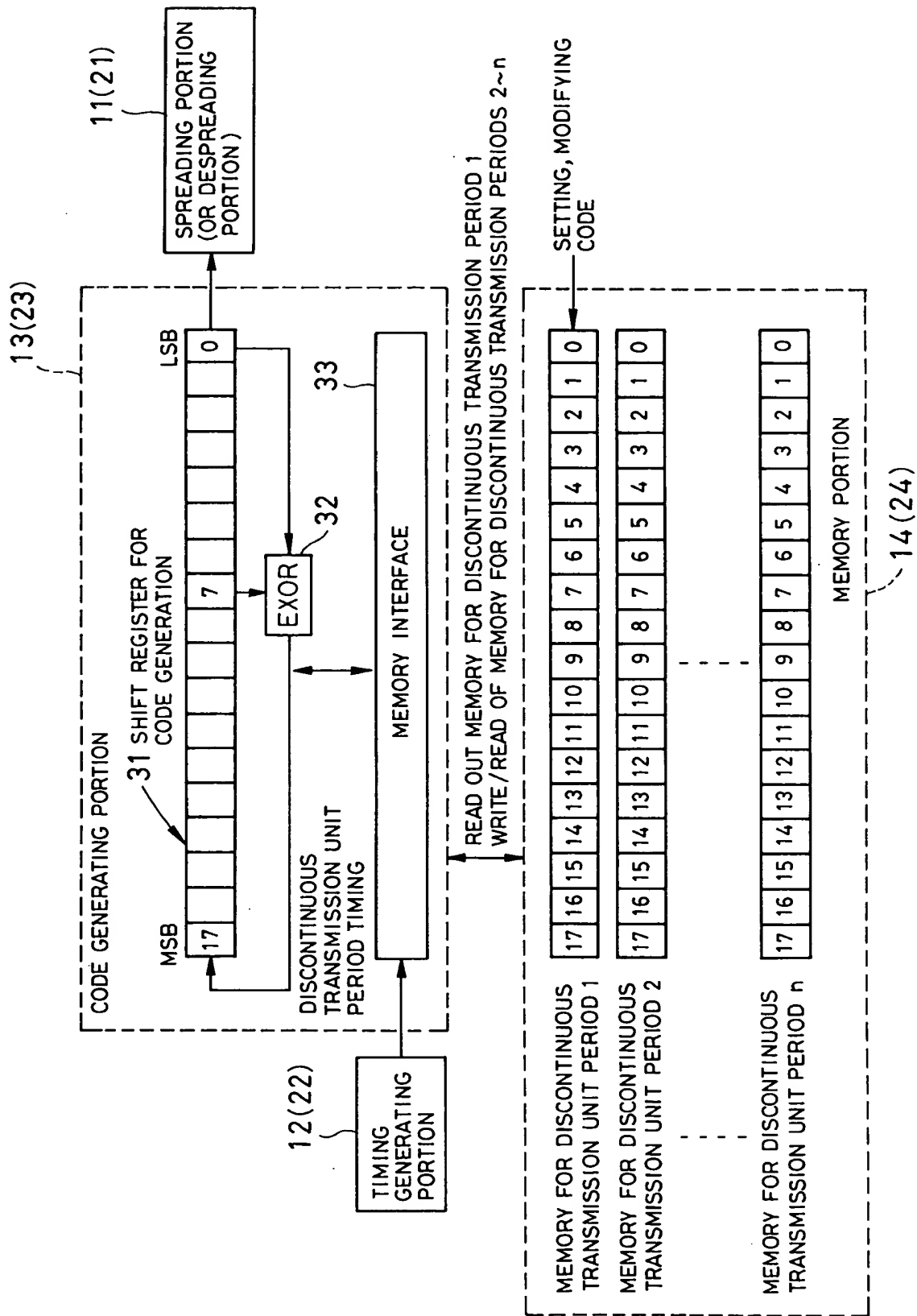
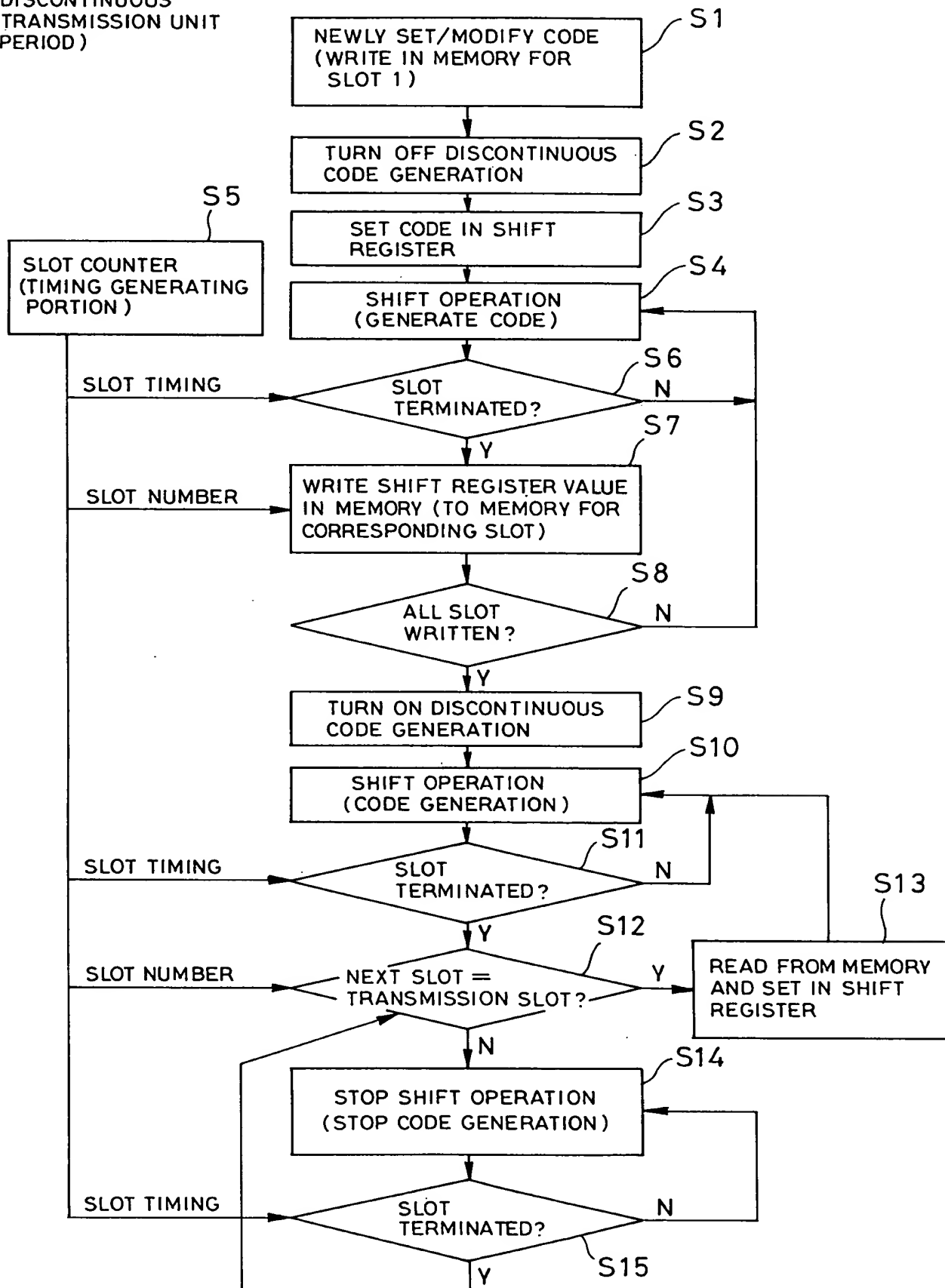


FIG. 4

(NOTE: SLOT MEANS  
DISCONTINUOUS  
TRANSMISSION UNIT  
PERIOD)



002060" 57 295950

FIG. 5

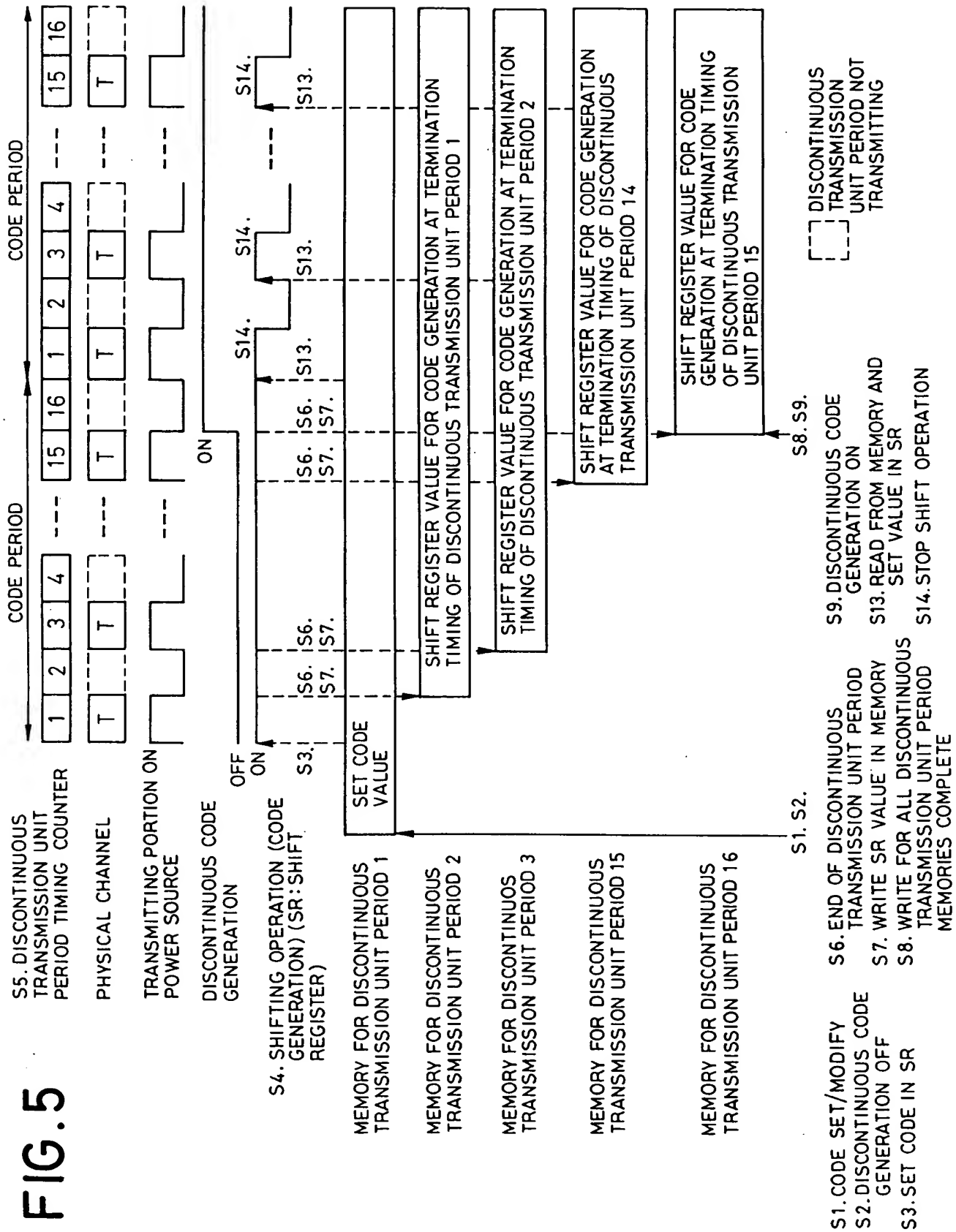


FIG. 6

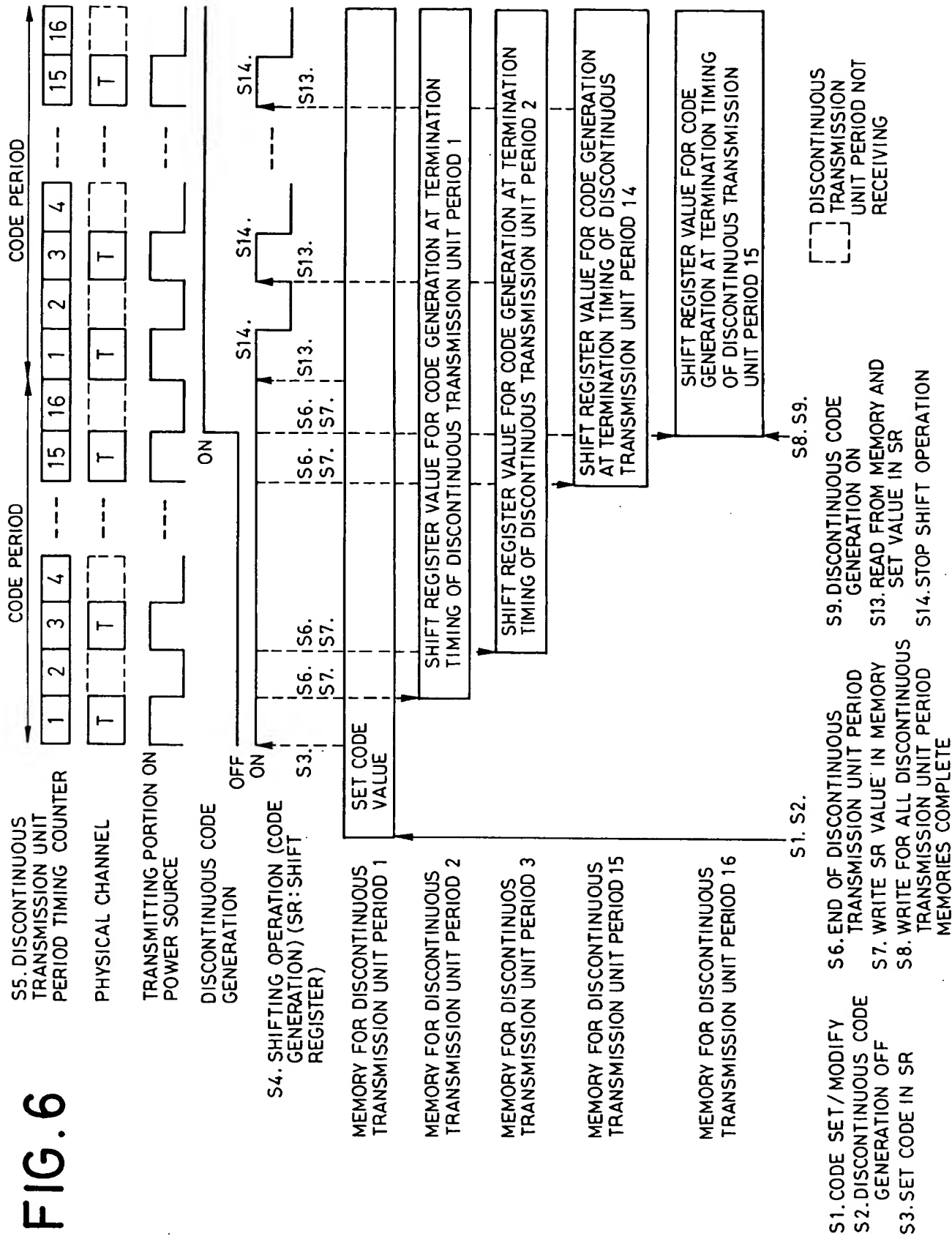


FIG. 7

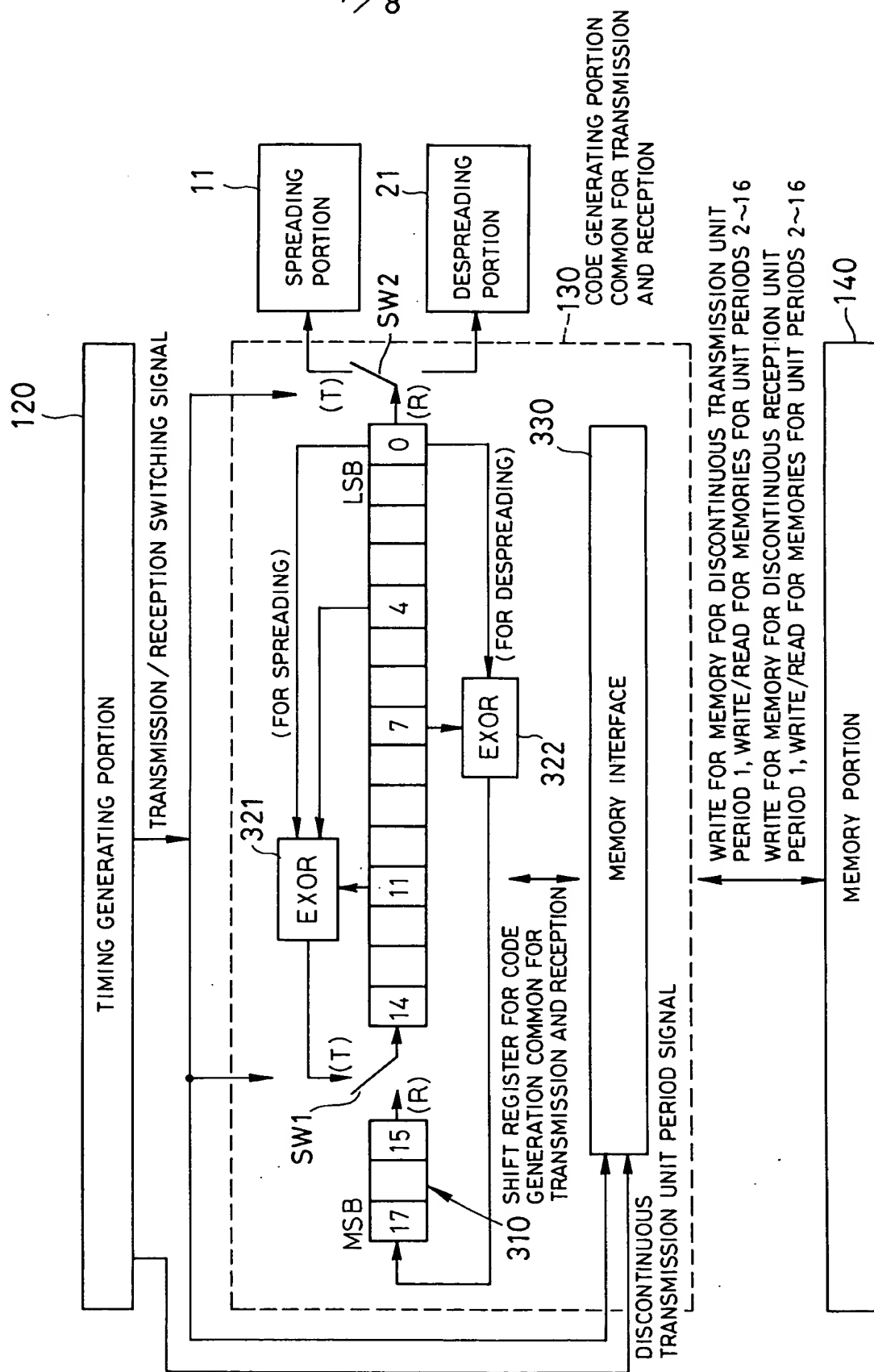


FIG. 8A

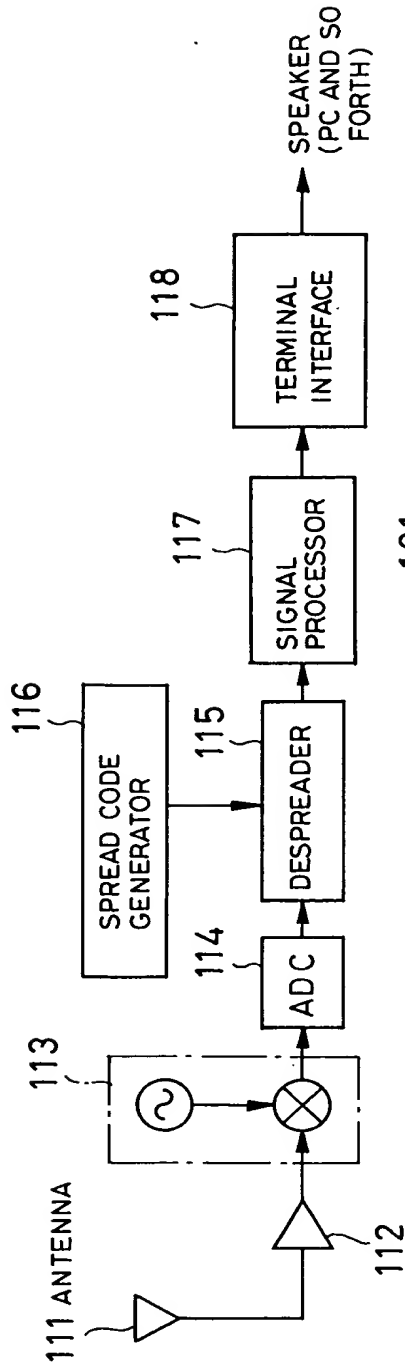


FIG. 8B

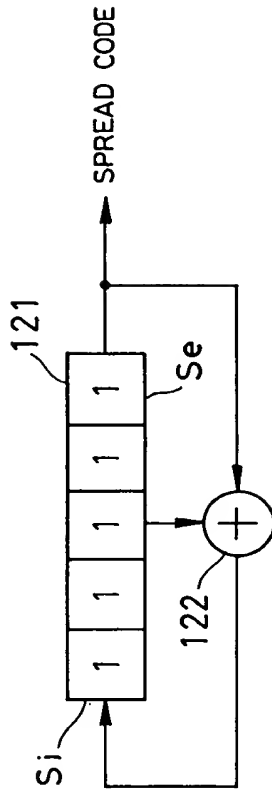


FIG. 8C

